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Sheet 1 of 1

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	ATTY. DKT. NO. 500.39521X00	SERIAL NO. 09/764487
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U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
	AA					
	AB					
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	AL					

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
at	AM	5-82787	4/1993	JP	H01L 29/784		X
	AN						
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

at	AU	IEICE Transactions, vol. E 74, pp.130-141, 1991
at	AV	IEEE International Solid State Circuit Conference, pp. 266-267, 1996
at	AW	K. Yano et al., "A Room Temperature Single Electron Memory Device Using Fine-Grain Polycrystalline Silicon",
	AX	IEDM 1993, pp. 541-544
	AY	
	AZ	
Examiner		Date Considered
[Signature]		01/05/03